




JSS Mahavidyapeetha
JSS Academy of Technical Education, Bengaluru
Department of Electronics & Communication Engineering



FACULTY PROFILE

1. Personal Details

NAME	Dr. Poornima N	
DEPARTMENT	Electronics & Communication Engineering	
DESIGNATION	Associate Professor	
PHONE	9845988280	
EMAIL ID	poorniman@jssateb.ac.in	
TEACHING EXPERIENCE	26 Years	
INDUSTRY EXPERIENCE	--	
RESEARCH EXPERIENCE	13 Years	

2. Qualification

COURSES	SPECIALIZATION	INSTITUTION	UNIVERSITY
PhD	Low Power VLSI Design	VIT University, Chennai Tamilnadu	VIT University, Chennai Tamilnadu
Mtech	VLSI Design and Embedded Systems	BMS College of Engineering Bangalore Karnataka, INDIA	Visvesvaraya Technological University , Belgavi,Karnataka
BE	Electronics and Communication	PES College of Engineering Mandya, Karnataka, INDIA	Mysore University, Karnataka

3. Membership of Professional Bodies:

4. Awards

Award Title	Date of receiving the award	Award issuing authority/ Body / Organization

4. Publications (Journals Conferences)

i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	Secure and private data sharing in CPS e-health systems based	Poornima N Shubha B T Hemalatha, A Bhuvaneswari, K Santhi, M Lawanyashri, Geeta C Mara	Measurement: Sensors,	pp.100787 Volume 27, June 2023 ISSN: 2665-9174	Scopus	1.08	Elsevier

	on CB-SMO techniques						
2	Implementation of blood Glucose and cholesterol monitoring device using non-invasive technique	Poornima N, Shubha B, Anuradha M G, Suprada H S, Prathiksha R V	EMITTER International Journal of Engineering Technology	Vol. 11, No. 1, June 2023, pp. 76-88	WoS	4.424	EEPIS
3	Design and Implementation of 32-Bit High Valency Jackson Adders	Poornima N & V S Kanchana Bhaaskaran	Journal of Circuits, Systems, and Computers (JCSC)	Vol. No.26 Issue No.7	WOS	0.595	WORLD SCIENTIFIC PUBL CO PTE LTD, SINGAPORE
4	Design of Energy Efficient High-Valency Jackson Flagged Adder Architectures	Poornima N & V S Kanchana Bhaaskaran	International Journal of Pure and Applied Mathematics	Vol. No.118 Issue No.22	--		Academic Publications, Kuwait
5	Threshold Compare and Load Balancing Algorithm to for Resource Optimization in a Green Cloud	Poornima N, Nagamani H Shahapure and Rekha P M	Revista Geintec-Gestao Inovacao E Tecnologias,	Vol. 11 No. 4 (2021) ISSN: 2237-0722	--		
6	Internet of Behaviour: A Sneak Peek	Poornima N, Nagamani H Shahapure and Shubha B	EFFLATOUNIA-Multidisciplinary Journal,	Volume: 5 Issue 2 Pages: 1669 – 1678	-	3.8	
7	Capacitor Less Low Dropout Voltage Regulator	Poornima N & Sriranganatha Sagar.K.N	International Journal of Electrical and Electronics Engineering (IJEET)	Vol. No. : 2 Issue : 2,3,4	--		
8	FPGA implementation and performance analysis of conventional and modified router design for NOC.	Poornima N & Shivakumar M	International Journal of Information Technology and Computer Engineering (IJITC)	Vol. No. : Issue : Special	--		
	Performance Analysis Of Parallel Prefix Adder	Poornima N & Manjunath Naik V	International Journal of Electrical, Electronics and Data Communication	Vol.3, Issue.7, pp 74-77	--		

9	IoT Based Gas Leakage Detector Robot	Poornima N & Ashwin M, Chinmaya A, Deepak Bhojani, Harshita Bhojani	International Journal of Research & Technology,	Vol. No. 8 Issue No. 3	--		
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ii. International Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	Power-Delay Optimized 32 Bit Radix-4, Sparse-4 Prefix Adder	Poornima N & V S Kanchana Bhaaskaran	Fifth International Conference on Signal and Image Processing (ICSIP), ,2014	doi: 10.1109/ICSIP.2014.38 ISBN: 978-0-7695-5100-5	WOS		IEEE
2	Area Efficient Hybrid Parallel Prefix Adders	Poornima N & V S Kanchana Bhaaskaran	International Conference on Nano materials and Technologies	ISBN:978-81-925751-8-6,	WOS		Elsevier
3	Review on Text and Speech Conversion Techniques based on Hand Gesture	Poornima N, Gunasagari G S, Abhijna Yaji, M Achuth, Anisha Maria Dsilva, RS Chethana	5th International Conference on Intelligent Computing and Control Systems (ICICCS)	ISBN: 978-0-7381-1327-2	Scopus	--	IEEE
4	Keller: Indian Sign Language based Gesture to Text and Speech Assistant for Covid-19 Protective Quotes	Poornima N, Gunasagari G S, Abhijna Yaji, M Achuth, Anisha Maria Dsilva, RS Chethana	2022 7th International Conference on Communication and Electronics Systems (ICCES)	pp. 241-247 ISBN:978-1-6654-9634-6	Scopus	--	IEEE
5	NLP Based Word Predictor for Dementia Patients: A Systematic Review	Poornima N, Nagamani H Shahapure, Spoorthi Kulkarni, V Shilpa, Rajiv Ranjan Singh, Patel Kavan	2022 6th International Conference on Intelligent Computing and Control Systems	pp.1538-1543 ISSN: 2768-5330 ISBN:978-1-6654-1035-9	Scopus	--	IEEE
6	Patronus: Fire Detection and Extinguisher System using Image Processing	Poornima N, GS Gunasagari, Ankitha S Bangar, Pokala Sai Sparsha Prasad,	2023 7th International Conference on Intelligent Computing and Control Systems (ICICCS)	pp. 1300-1306, ISBN :979-8-3503-9725-3 ISSN: 2768-5330	Scopus	--	IEEE

		Poorvi Pai, Tanaya Mehta					
7	Emotion Recognition based on PPG and GSR Signals using DEAP Dataset	Poornima N, Shubha B, Vachana M Gowda, Sushma U, Meghana Y R, Bhoomika T S	2023 International Conference on Network, Multimedia and Information Technology	pp. 1-7 ISBN: 979-8-3503-0082-6	Scopus	--	IEEE
8	FPGA implementation and performance analysis of conventional and modified router design for NOC.	Poornima N & Shivakumar M	4th National Conference on Recent Advances in Science, Engineering & Technology (NCRASET-16) SVCE,B'LORE	--	--	--	--

5. Grants/Funding Received

Grant Amount	Project Name	Date of receiving the Grant	Grant issuing authority/ Body / Organization
7000	KELLER: Indian Sign Language Gesture to Text and Speech Assistant	2021	KSCST- 44 th SERIES OF STUDENT PROJECT PROGRAMME
6000	SANIHA: Application To Create Virtual Relationship Between Farmers And Consumers	2022	KSCST- 45 th SERIES OF STUDENT PROJECT PROGRAMME
200000	Research Applications in Machine Learning and Artificial Intelligence	2023	VGST-KSTePS

6. Others :Workshops / Conference (Orgaineds/Attended)

6.a. Workshops / Conference organized

	FDP on “Research Applications in Artificial Intelligence (AI) and Machine Learning (ML)” funded by VGST from 11th Sept to 15th Sept, 2023

6.b. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

National Workshop on Accreditation Process for Technical Institutions organized by JSSATE Bangalore in association with ISTE,IIIE, QCFI from 26-08-2016 to 27-08-2016
International Conference on Nano materials and Technologies organized by Vardhamaan College of Engineering , Hyderabad sponsored by BARC, Mumbai
Workshop on “How to conduct Student Induction Program” organized by AICTE on 20th September 2021
FDP on “Inculcating Universal Human Values in Technical Education” organized by AICTE from 12th June to 16th June 2023
FDP on Analog VLSI Design using EDA Tools organized by AICTE and VTU from 20th to 25th March, 2023

6.c. workshop/ Seminar /Conference Attended (those NOT sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

38 th International conference on VLSI Design and 24 th International conference organized at Leela Palace, Bangalore from 4-01-2025 to 8-01- 2025
International Conference on Networks, Multimedia and Information Technology at Nitte Meenakshi Institute of Technology, Bengaluru from 01-02, September 2023
7th International Conference on Intelligent Computing and Control Systems (ICICCS) at Vaigai College of Engineering, Madurai, India from 17 th May 2023 to 19 th May 2023
FDP on Importance of Data Science In Machine Learning And Its Applications organized by RV Institute of Technology and Management from 6th to 10th Feb, 2023
FDP on Analytical Techniques for Nano materials organized by NIT Warangal and SJCE from 18th July to 25th July, 2022
FDP on “Python Programming for Artificial Intelligence, High Performance Computing and data analytics organized by NIT Warangal from 20 th June to 1 st July 2022
International Conference on Communication and Electronics Systems organized by PPG Institute of Technology, Coimbatore from 22 nd June 2022 to 24 th June2022
International Conference on Intelligent Computing and Control Systems (ICICCS) at Vaigai College of Engineering, Madurai, India from 25 th May 2022 to 27 th May 2022
International Conference on Intelligent Computing and Control Systems (ICICCS 2021) organized by Vaigai College of Engineering, Madurai from 6th to 8th May,2021
International Conference on Advanced Computing and Information Technology, IACIT-2021 organized by REVA University, Bengaluru on 17 th May, 2021
AI and ML applications in Image processing using modern tools organized by M S Ramaiah Institute of Technology, Bangalore from 13-07-2020 to 18-07-2020
Machine Learning for All 4 weeks online credit score authorized by University of London and offered through Coursera
Analog and Digital System Design Using CADENCE Tool organized by GSSS Institute of Engineering and Technology, Mysore from 23-01-2019 to 25-01-2019
Signal , Image processing and SDR using Lab View organized by JSSATE Bangalore in association with TECHLABS from 22-06-2015 to 24-06-2015
International conference on Signal and Image processing organized by BNMIT Bangalore from 8-01-2014 to 10-01-2014
National conference on VLSI, Signal and Image processing organized by JSSATE Bangalore from 22-08-2014 to 23-08-2014
Advanced VLSI Design using Cadence Tool Suite organized by Cadence Design Systems India Pvt. Ltd from 16-07-2014 to 18-07- 2014
International conference on VLSI Design Embedded Solutions for Emerging markets-Consumer, Energy, Automotive organized at Hyderabad International Convention Center, Hyderabad from 7-01-2012 to 11-01-2012
National conference on Antennas and its applications organized by VTU, Belguam from 24-02-2012 to 25-02-2012
Advanced trends in Low Power SoC VLSI Design organized by Jain University from 5-10-2012 to 6-10-2012
FPGA Based Embedded System Design organized by JSSATE Bangalore and DEXEL EmDAC on 3-01- 2012
VLSI, MEMS, and Integration organized by VIT, Chennai on 09-10-2011
Analog and Mixed mode Design using Cadence Tool organized by BNMIT Bangalore and CADENCE, Design systems from 2-02-2009 to 4-02-2009
Recent trends in VLSI and Embedded System Design Technology organized by Global Academy of Technology, CADENCE, UTL from 12-04-2007 to 13-04-2007

6.d. PROJECT / Innovations/Patents

Patent titled ARTIFICIAL INTELLIGENCE BASED MANAGEMENT OF AUTONOMOUS GUIDED VEHICLE FOR SMART INDUSTRIAL MANUFACTURING published on 18 th June 2021
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6.e. Any other information you will like to share about your professional experience